

## **Curriculum Vitae**

Prof. Dr. Ferit Acar Savacı

Date and place of birth: 19.1.1958, İZMİR

### **Education**

- 1998            Proffessor at IYTE
- 1992            Associate Professor
- 1990-1992     Post Doc.: Katholieke Universiteit (KUL), Belgium
- 1989            Ph.D. : Istanbul Technical University (ITU), Istanbul
- 1982            M.S. : Bogazici University, Istanbul
- 1979            B.S. : Middle East Technical University (METU), Ankara
- 1975            Grand Blanc High School, Flint, Michigan (High School Diploma)
- 1974            İzmir Atatürk High School
- 1971            Karataş High School, İzmir

### **Profession**

- 1998 - ...     Izmir Institute of Technology (IYTE), Electrical and Electronics Engineering Department – Professor
- 1993–1998     ITU Electrical and Electronics Faculty, Circuits and Systems Division – Associate Professor
- 1990–1993     ITU Electrical and Electronics Faculty, Circuits and Systems Division – Assistant Proffessor
- 1990-1992     Katholieke Universiteit, Leuven, Belgium – Post. Doc. On Neural Networks at ESAT-SISTA group
- 1982-1990     ITU Electrical and Electronics Faculty, Circuits and Systems Division
- 1981-1982     Boğaziçi University – Student assistant

## **Other Profession**

- 1996-1997 Kocaeli University, Electrical and Electronics Engineering Department (Circuit Analysis courses on Spring and Fall semesters)
- 1995 Firat University, Electrical and Electronics Engineering Department (Graduate course on Neural Networks in summer school)
- 1985-1989 ITU, Mathematic Engineering Department (Course on Introduction to System Analysis)
- 1984-1985 Sakarya University, Electrical and Electronics Engineering Department (Course on Circuit Theory)
- 1980-1981 (Part-time) Engineer, SESA, EMI, Istanbul

## **Management**

- From January 1999 Izmir Institute of Technology (IYTE), Senate Membership
- From January 1998 Izmir Institute of Technology (IYTE), Head of Electrical and Electronics Engineering Department
- From January 1998 Izmir Institute of Technology (IYTE), Faculty of Engineering Administrative Board

## **Courses Instructed**

### Undergraduate:

Circuit Analysis

Introduction to System Analysis

Analysis of Engineering Systems

Discrete Time Signals and Systems

Signals and Systems

Digital Filters

Feedback Control Systems

## Graduate:

Linear System Theory

Mathematical Methods in Engineering

Artificial Neural Networks

Nonlinear System Theory

Fractal Geometry

## **Scholarships**

- 1974            One of the top 100 student in University Selection Exam
- 1975            Ranking in General Motors Mathematics Competition
- 1975-1979     TUBITAK University Undergraduate Scholar
- 1979            METU, Faculty of Engineering, Electronic Department, Honour Student
- 1982-1989     TEV, Ph.D. Scholar

## **Projects**

“Secure Communication by Synchronized Chaotic Circuits” DPT (1995-1998,ITU)

“Yapıların Dinamik Davranışlarının Amörtisörlerle İyileştirilmesi” DPT (2005,IYTE)

“Zaman-Frekans Aralığında, Bağımsız Bileşen Analizi ve Dalgacık Dönüşümü Kullanarak Ses İşaretlerinin Gözü Kapalı Ayrıştırılmasını ve Tanımlanmasının Sayısal İşaret İşlemcilerle Gerçek Zamanda Gerçeklenmesi”

## **MSc. And Ph.D. Theses**

“Analysis of Switched Capacitor Networks”, Boğaziçi University, Electrical Engineering Department, Msc. Thesis, June 1982.

“Fault Diagnosis in Linear Circuits: A Parameter Identification Approach”, ITU, Graduate School of Science, Engineering And Technology, Electronics and Communications Engineering Department, Ph.D. thesis, February 1989.